

Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09703792 **Filing Dt:** 11/02/2000**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** NONE**Pub Dt:****Inventors:** Shinji Hayakawa, Satoshi Watanabe, Atsushi Shimbo, Hiromi Aoyagi**Title:** Packet receiver with the influence of jitter and packet losses reduced before a buffer becomes idle due to data delays and packet receiving method using the same**Assignment: 1****Reel/Frame:** 011264 /
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5**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** HAYAKAWA, SHINJI**Exec Dt:** 10/04/2000WATANABE, SATOSHI**Exec Dt:** 10/04/2000SHIMBO, ATSUSHI**Exec Dt:** 10/06/2000AOYAGI, HIROMI**Exec Dt:** 10/06/2000**Assignee:** OKI ELECTRIC INDUSTRY, CO. LTD.7-12, TORANOMON 1-CHOME
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<u>L70</u>	telephone and L67	0	<u>L70</u>
<u>L69</u>	voice and L67	1	<u>L69</u>
<u>L68</u>	speech and L67	0	<u>L68</u>
<u>L67</u>	dummy and (4707831.pn. or 4607363.pn. or 6091709.pn.)	1	<u>L67</u>
<u>L66</u>	L9 same threshold\$	4	<u>L66</u>
<u>L65</u>	(end\$6 near1 discard\$6) and 6473432.pn.	0	<u>L65</u>
<u>L64</u>	decod\$6 same (discard\$6 near1 (start\$6 or begin\$6))	54	<u>L64</u>
<u>L63</u>	decod\$6 and (6658027.pn. or 5914936.pn.)	1	<u>L63</u>
<u>L62</u>	decod\$6 and L57	0	<u>L62</u>
<u>L61</u>	4607363.uref. or 4707831.uref.	78	<u>L61</u>
<u>L60</u>	threshold\$6 and L57	1	<u>L60</u>
<u>L59</u>	(read\$6 same threshold\$) and L57	0	<u>L59</u>
<u>L58</u>	((read\$6 near1 (start\$6 or begin\$6)) same threshold\$) and L57	0	<u>L58</u>

L57	4607363.pn. or 4707831.pn.	2	L57
L56	decod\$6 same (discard\$6 near1 (start\$6 or begin\$6)) same (queue\$6 or buffer\$6 or memor\$6)	12	L56
L55	(quality near1 sound) same (discard\$6 near1 (start\$6 or begin\$6)) same (queue\$6 or buffer\$6 or memor\$6)	0	L55
L54	L53 and (sound or soundless or speech)	3	L54
L53	(discard\$6 near1 (start\$6 or begin\$6)) same (length near4 (queue\$6 or buffer\$6 or memor\$6))	11	L53
L52	(sound or soundless or speech) same L51	0	L52
L51	(discard\$6 near1 (start\$6 or begin\$6)) same (discard\$6 near1 (end\$6 or stop\$6))	23	L51
L50	(discard\$6 near1 (end\$6 or stop\$6))	1186	L50
L49	(length near1 (queue\$6 or buffer\$6 or memor\$6))	10991	L49
L48	(decod\$6 near1 start\$6 near1 (point\$6 or threshold\$))	100	L48
L47	L36 same (queue\$6 or buffer\$6 or memor\$6)	7	L47
L46	(length\$6 near4 (queue\$6 or buffer\$6 or memor\$6)) same (discard\$6 near1 (start\$6 or end\$6))	7	L46
L45	(length\$6 near4 (queue\$6 or buffer\$6 or memor\$6)) same L42	0	L45
L44	(diminish\$6 or diminishment) and L42	1	L44
L43	(diminish\$6 or diminishment) same (discard\$6 near1 (start\$6 or end\$6))	4	L43
L42	decod\$6 same (discard\$6 near1 (start\$6 or end\$6))	54	L42
L41	decod\$6 same L33	0	L41
L40	L39 same L38	0	L40
L39	(read\$6 or decod\$6) near1 start\$6 near1 (point\$ or threshold\$)	524	L39
L38	(standard near1 deviation)	79234	L38
L37	L36 same threshold\$	4	L37
L36	((end\$6 or stop\$6) near1 discard\$6) same ((start\$6 or begin\$6) near1 discard\$6)	23	L36
L35	(end\$6 near1 discard\$6) same (start\$6 near1 discard\$6)	3	L35
L34	(end\$6 near1 discard\$6) same (start\$6 near1 discard\$6) same threshold\$	0	L34
L33	discard\$6 same ((start\$6 or end\$6) near1 threshold\$)	14	L33
L32	(disminishment or disminish\$6) same discard\$6 same ((start\$6 or end\$6) near1 threshold\$)	0	L32
L31	(disminishment or disminish\$6) same L30	0	L31
L30	(discard\$6 near1 (start\$6 or end\$6) near1 threshold\$)	6	L30
L29	L27 same L28	1	L29
L28	(length\$6 near4 (queue\$6 or buffer\$6 or memor\$6))	36712	L28
L27	read\$6 same (start\$6 near1 threshold\$)	122	L27
L26	(read\$6 near1 threshold\$) same (length\$6 near4 (queue\$6 or buffer\$6 or memor\$6))	7	L26
L25	(read\$6 near1 (start\$6 or begin\$6)) same (queue\$6 or buffer\$6 or memor\$6) same threshold\$	237	L25
L24	L22 same (length\$6 near4 (queue\$6 or buffer\$6 or memor\$6))	8	L24

<u>L23</u>	(queue\$6 near4 length\$) same L22	0	<u>L23</u>
<u>L22</u>	((read\$6 near1 (start\$6 or begin\$6)) same threshold\$) or (read\$6 near1 threshold\$)	2765	<u>L22</u>
<u>L21</u>	(buffer\$6 or memor\$6 or queue\$6) same length\$ same L19	0	<u>L21</u>
<u>L20</u>	(queue\$6 near4 length\$) same L19	0	<u>L20</u>
<u>L19</u>	(read\$6 near1 start\$6 near1 threshold\$)	9	<u>L19</u>
<u>L18</u>	(queue\$6 near4 length\$) same threshold\$	625	<u>L18</u>
<u>L17</u>	(queue\$6 near1 length\$) same ((diminishment or diminish\$6 or decod\$6) near1 (start\$6 or begin\$6 or end\$6))	1	<u>L17</u>
<u>L16</u>	(queue\$6 near1 length\$) same ((diminishment or diminish\$6) near1 (start\$6 or begin\$6 or end\$6))	0	<u>L16</u>
<u>L15</u>	(queue\$6 near1 length\$) same ((decode or decod\$6) near1 (start\$6 or begin\$6))	1	<u>L15</u>
<u>L14</u>	(queue\$6 near1 length\$) same ((decode or decod\$6) near1 start\$6)	0	<u>L14</u>
<u>L13</u>	(queue\$6 near1 length\$) same ((decode or decod\$6) near1 start\$6 near1 point\$)	0	<u>L13</u>
<u>L12</u>	(decode or decod\$6) near1 start\$6 near1 point\$	99	<u>L12</u>
<u>L11</u>	(disminishment or disminish\$6) near1 control\$6	0	<u>L11</u>
<u>L10</u>	L9 same threshold\$	4	<u>L10</u>
<u>L9</u>	((start\$6 or begin\$6) near1 discard\$6) same ((end\$6 or stop\$6) near1 discard\$6)	23	<u>L9</u>
<u>L8</u>	((start\$6 or begin\$6) near1 (diminishment or diminish)) same ((end\$6 or stop\$6) near1 (diminishment or diminish))	3	<u>L8</u>
<u>L7</u>	(discard\$6 near1 (end\$6 or start\$6)) same L6	2	<u>L7</u>
<u>L6</u>	discard\$6 same threshold\$ same start\$6 same end\$6	108	<u>L6</u>
<u>L5</u>	(discard\$6 near4 start\$6) same (discard\$6 near4 end\$6) same threshold\$	0	<u>L5</u>
<u>L4</u>	(discard\$6 near4 (end\$6 or start\$6)) same threshold\$	149	<u>L4</u>
<u>L3</u>	(discard\$6 near1 start\$6) same (discard\$6 near1 end\$6) same threshold\$	0	<u>L3</u>
<u>L2</u>	((disminishment or disminish\$6) near1 start\$6) same (decod\$6 near1 start\$6)	0	<u>L2</u>
<u>L1</u>	(disminishment or disminish\$6) near1 start\$6 near1 point\$	0	<u>L1</u>

END OF SEARCH HISTORY